

Notice of References Cited	Application/Control No. 09/489,895	Applicant(s)/Patent Under Reexamination HENKEL ET AL.	
	Examiner Dwin M Craig	Art Unit 2123	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,075,932 A	06-2000	Khouja et al.	716/4
	B	US-6,397,170 B1	05-2002	Dean et al.	703/14
	C	US-6,151,568 A	11-2000	Allen et al.	703/14
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	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Chaitali Chakrabarti and Dinesh Gaitonde, "Instruction Level Power Model of Microcontrollers", IEEE 1999, pages 76-79.
	V	Vivek Tiwari, Shadrad Malik, Andrew Wolfe, "Power Analysis of Embedded Software: A First Step Towards Software Power Minimization" IEEE 1994, pages 437-445.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.